PMShifter: Enabling Persistent Memory Fluidness in Linux

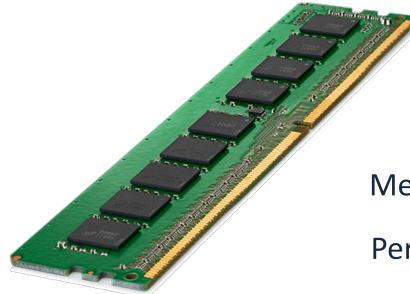
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System and Architecture Lab on Scalability, Reliability and Energy-Efficiency Department of Computer Science & Engineering University of California, San Diego



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Memory costs in datacenters

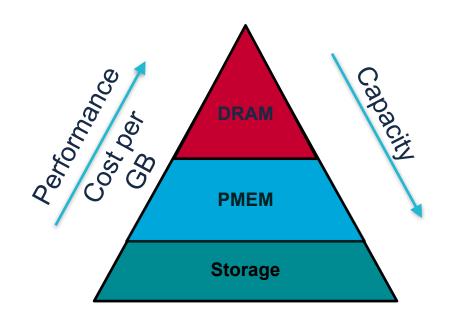


Memory accounts for 40-50% of modern data center costs

Persistent Memory (PMEM) can help alleviate these costs

Persistent Memory in storage stack

- 2x higher latency
- 7x lower bandwidth
- 8x larger capacity
- Fraction of the cost

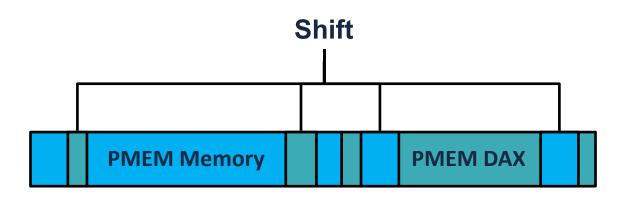


Persistent Memory Operation Modes

PMEM Memory	PMEM DAX

Memory and DAX at the same time

Persistent Memory Operation Modes



Division through command line Access to machine

Impractical!

Wasteful!

Our idea: PMShifter

PMEM Memory PMEM DAX

Key Idea: Dynamic shifting to utilize unused PMEM DAX chunks

PMShifter

Enables dynamic PMEM shifting

Proposes associated memory management fixes

- Accelerates compaction and migration
- Alleviates fragmentation
- Fixes PMEM issues in NUMA

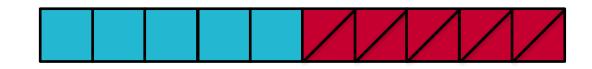
The rest of the talk

- Background & Motivation
- PMShifter
- Evaluation
- Conclusion

Overview

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- Increasing/decreasing size of available memory during runtime (e.g. faulty DIMMs, capacity on demand) on a region granularity (e.g. 2GB)
- PMShifter uses memory hot(un)plugging to shift between memory and DAX
- Memory regions are hot(un)plugged by **onlining/offlining** pages within





Online page



Offline page





Online page



Offline page





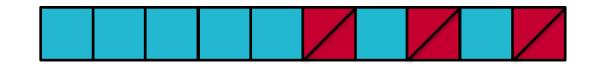
Online page



Offline page

Onlining or offlining free page:Update page metadata

Goal: Minimize offlining allocated pages





Online page



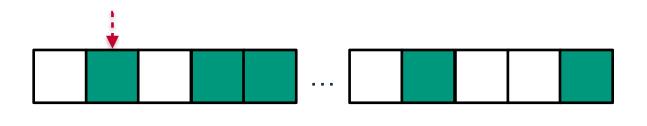
Offline page

Offlining an allocated page:

- Allocate a new page
- Copy contents
- TLB entries invalidated
- Update page metadata

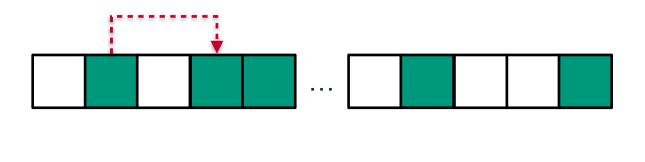
- The process for tackling memory fragmentation, a key memory management issue
- A fragmented memory can **increase** allocation latency by ~**3x**
- Based on an iterative 3-step process

Step 1: Gather allocated pages from start of 2MB block (start of address space)



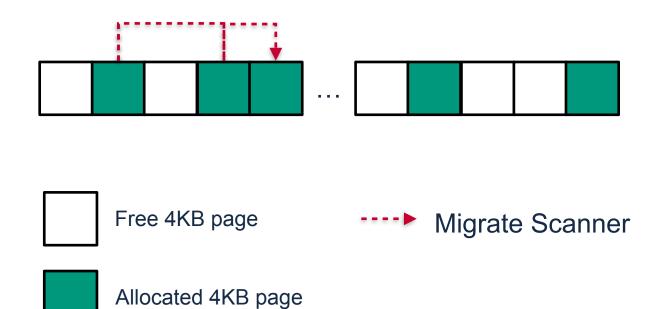


Step 1: Gather allocated pages from start of 2MB block (start of address space)

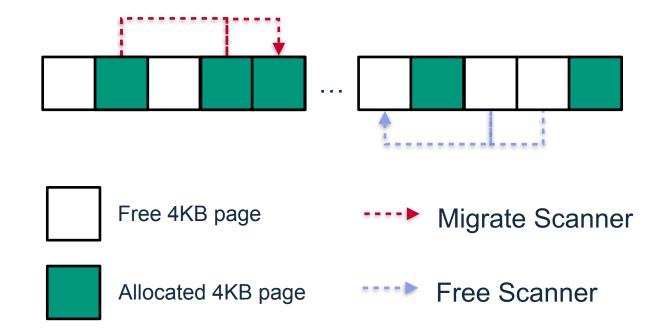




Step 1: Gather allocated pages from start of 2MB block (start of address space)



Step 2: Scan for free pages from end of 2MB block (end of address space)



Step 3: Migrate Pages





Allocated 4KB page

- **2MB** block granularity
 - Migrate scanner start from **start** of address space
 - Free scanner starts from **end** of address space
- Pages in migrate and free lists are *invisible*
- Compaction threshold
 - Next compaction run, scanners continue from last stop
 - Position reset when scanners meet

1. Unmovable pages lead to wasted cycles





Free 4KB page

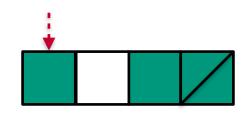


Allocated 4KB page



Pinned allocated 4KB page

1. Unmovable pages lead to wasted cycles





Free 4KB page



Allocated 4KB page

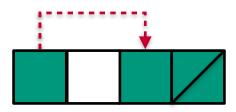


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Pinned allocated 4KB page

1. Unmovable pages lead to wasted cycles





Free 4KB page

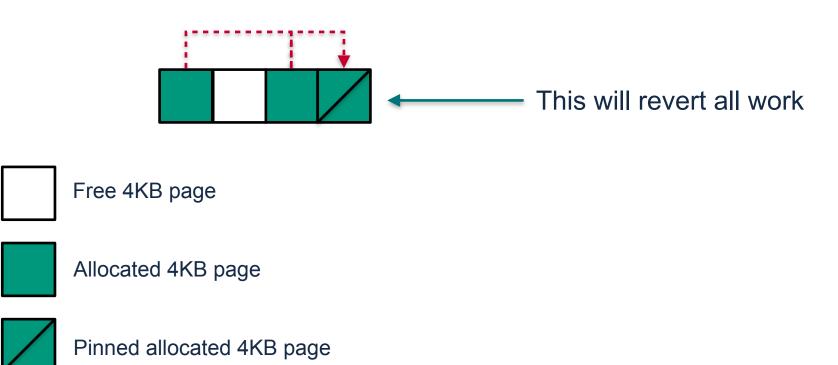


Allocated 4KB page



Pinned allocated 4KB page

1. Unmovable pages lead to wasted cycles

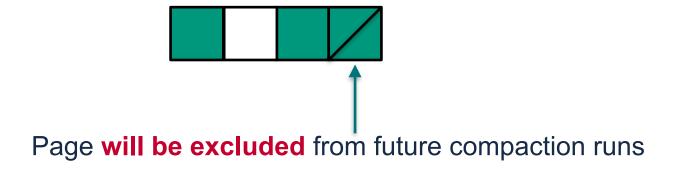


2. Free scanner skips:

- ≥ 2MB blocks (2MB and 4MB blocks are the biggest blocks)
- Small blocks that cannot accommodate all pages from migration list

Forces preemptive scanners meet and creates mixed space

3. Unfair page skip



PMEM Page Migration

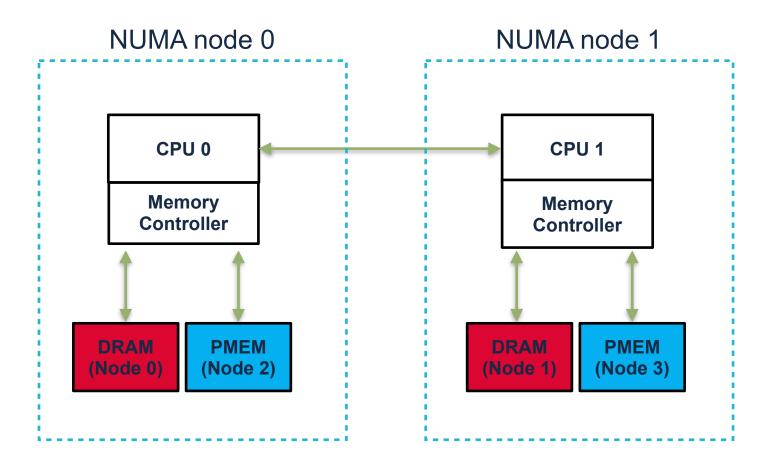
Crucial operation in hybrid memories

Process:

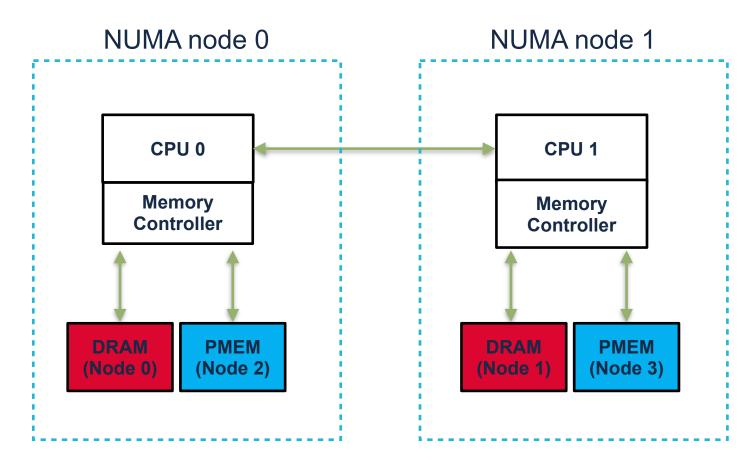
- Allocate a new page in the target memory
- Copy contents
- Free the old memory/TLB/metadata update

Main Linux allocator

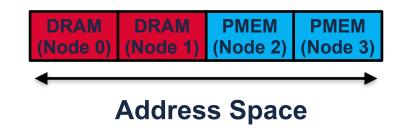
- Centralized, performance critical component
- Free page is closer to start of address space, will be migrated again from the compactor

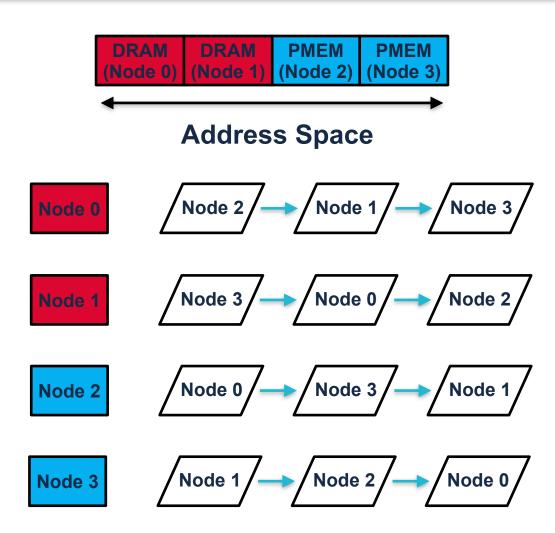


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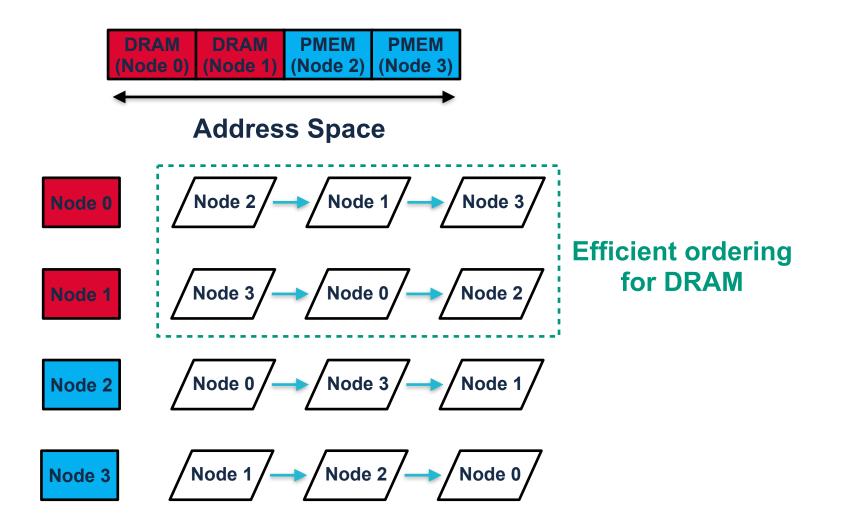


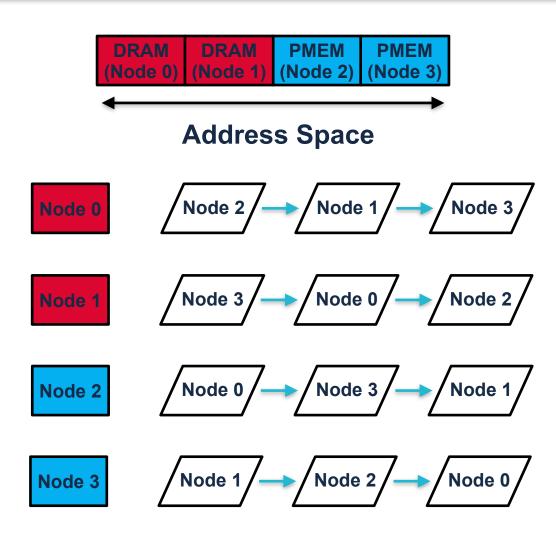
- 1. Accessing a local memory is faster than accessing a remote.
- 2. Accessing a remote DRAM is faster than accessing a remote PMEM.



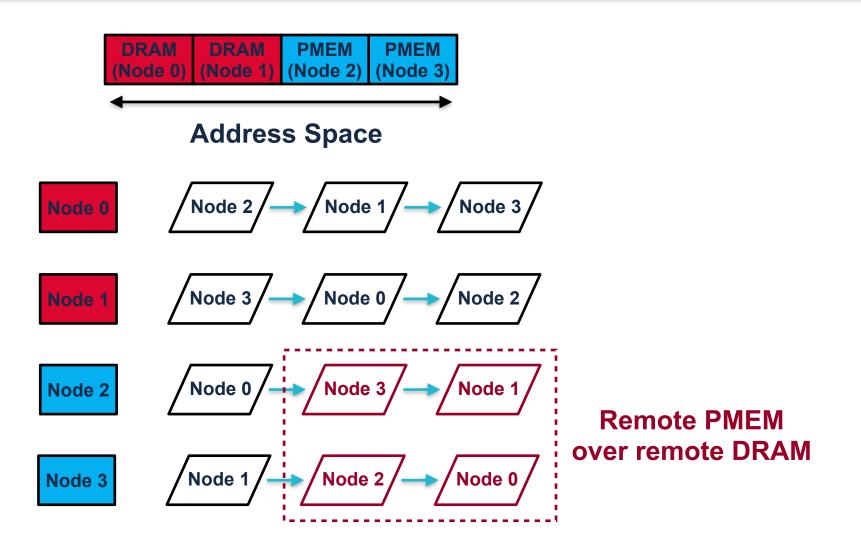


Non-Uniform Memory Access (NUMA) ordering





NUMA issue with PMEM



Overview

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PMShifter compaction

Same 3-step logic with Linux for compatibility

PMShifter compaction

Same 3-step logic with Linux for compatibility

Linux	PMShifter
Block-to-block logic	In bulk operation
2MB block	4MB block
Wasted cycles due to unmovable pages	Skip unmovable pages in O(1)
Free scanner skips ≥ 2MB and small blocks	Free scanner uses all blocks
Unfair page skip	Page state not maintained

PMShifter compactors

Different goals in DRAM and PMEM

- In DRAM maximize biggest free blocks
- In PMEM keep the start of the address space clean

The intuition behind this relates to shifting

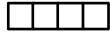
During compaction, fill half of the migrate list with the hottest pages from PMEM

- Increasing the total amount of migrated pages, increases throughput^[1]
- Avoid pressure in Linux allocator
- Accurate page placement

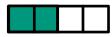
[1] Yan et al. "Nimble Page Management for Tiered Memory Systems" ASPLOS 2019

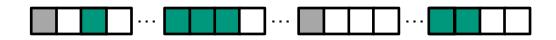
Step 1: DRAM pages from topN less loaded 4MB blocks

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Step 1: DRAM pages from less topN 4MB blocks

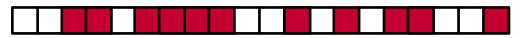


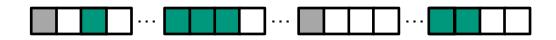


Migrate List



Step 2: Hot pages from PMEM

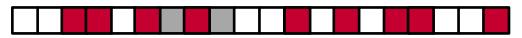




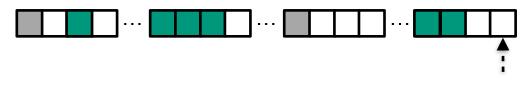
Migrate List

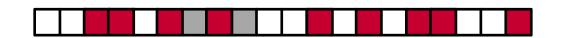


Step 2: Hot pages from PMEM



Step 3: Scan for free pages

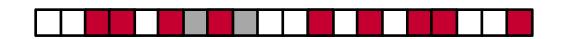






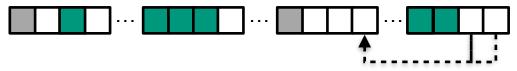
Step 3: Scan for free pages

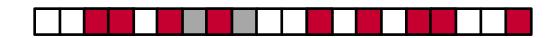






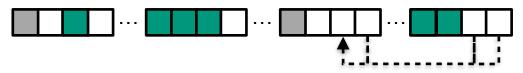
Step 3: Scan for free pages

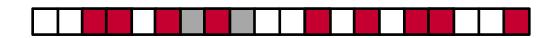




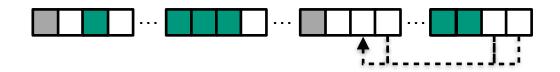


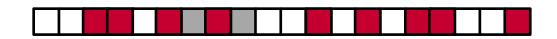
Step 3: Scan for free pages









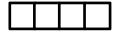


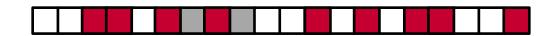
Migrate List



Step 4: Migrate Pages







Persistent Memory Shifting

PMEM shifting is a costly operation that should occur infrequently

Goal: Accurately predict if we need to acquire/release memory

Use an adjusted version of the Exponential Moving Average

Persistent Memory Shifting

Memory pressure at time t total free space

$$MP_t = a * free_space + (1 - a) * MP_{t-1}$$

smoothing factor

If *MPt* > threshold, **increase free memory by 5x**

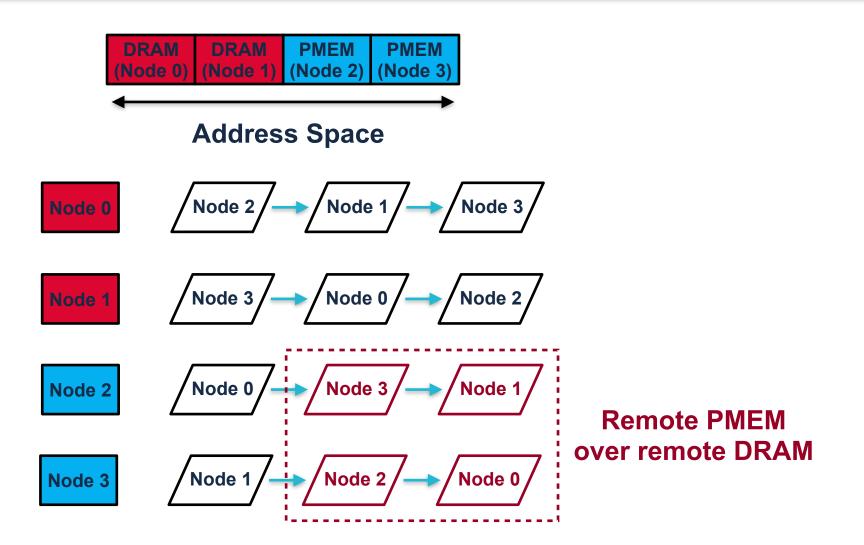
Persistent Memory Shifting

Which pages to shift, to reduce the cost?

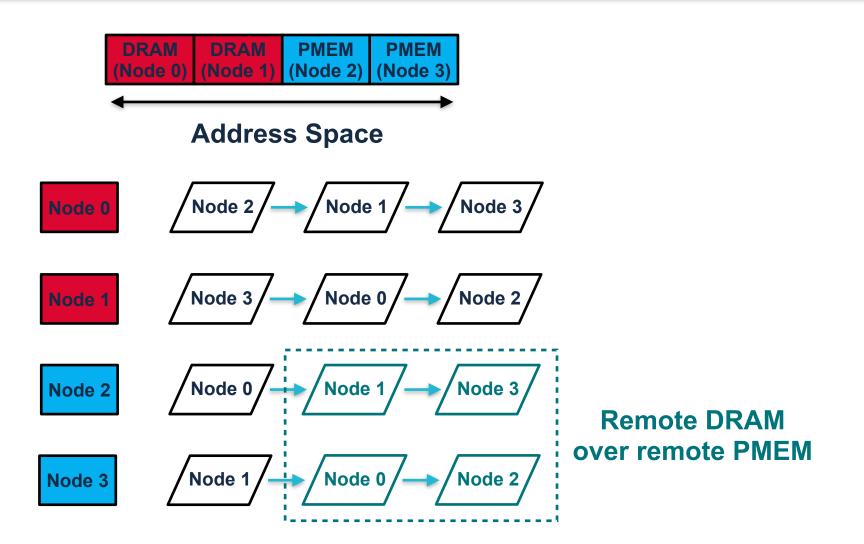
Reminder: The PMEM compactor keeps the start of address space clean



Proposed fix for NUMA



Proposed fix for NUMA



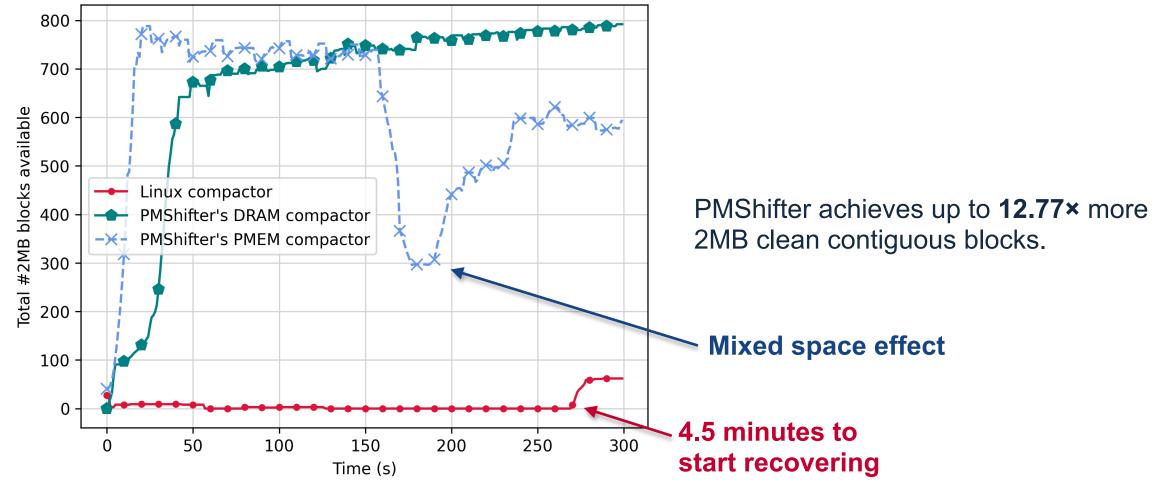
Overview

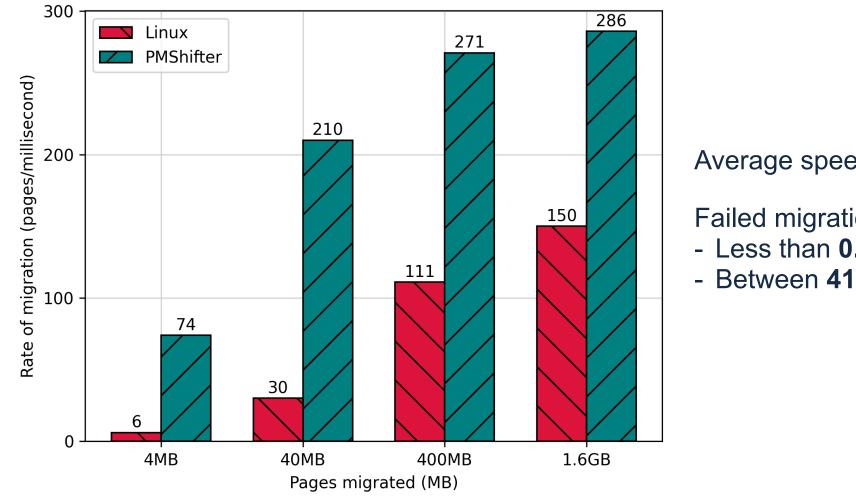
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Evaluation

- Implemented PMShifter in Linux v5.6.19
- Evaluated with
 - Microbenchmarks
 - Redis
 - Galois

Compaction performance





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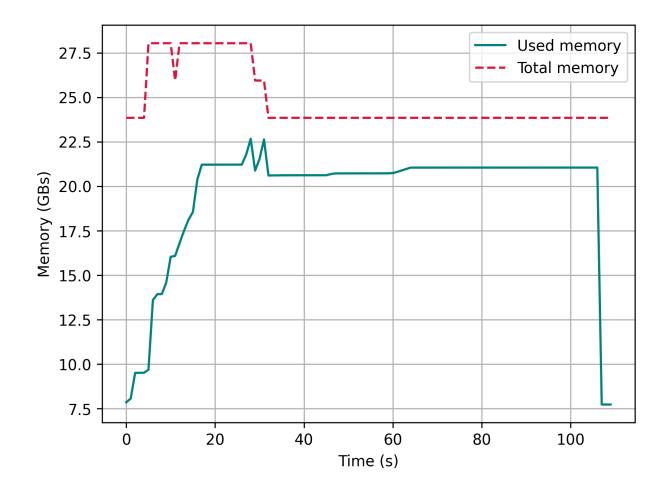
Average speedup: 5.88×

Failed migrations:

- Less than 0.0083% for PMShifter
- Between **41.4%** and **49.9%** for Linux

59

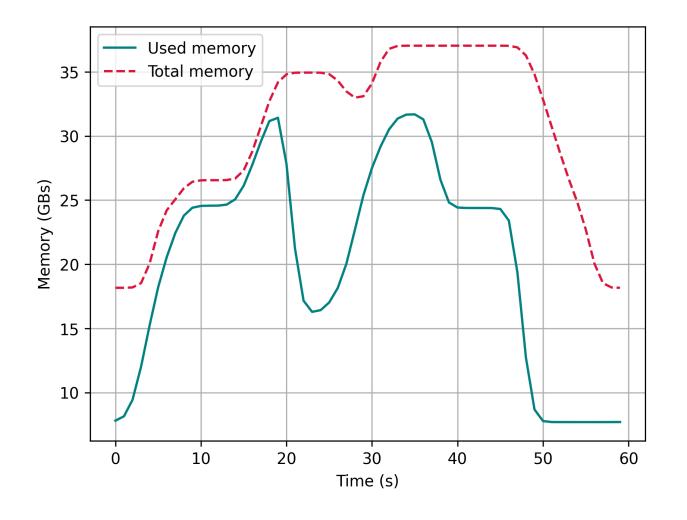
PMShifter elasticity



PMShifter elasticity

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PMShifter is elastic and proactive

Overview

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Conclusion

Is dynamic and elastic

Proposes associated memory management fixes

- Accelerates page migration
- Significantly **improves** fragmentation
- Fixes PMEM issues in NUMA